

Fig.1

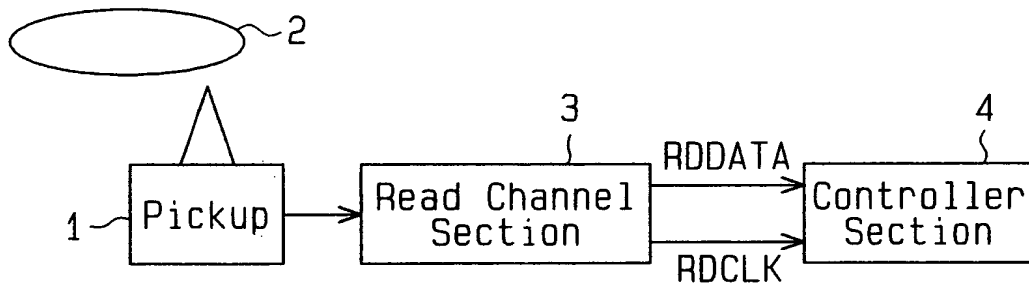


Fig.2

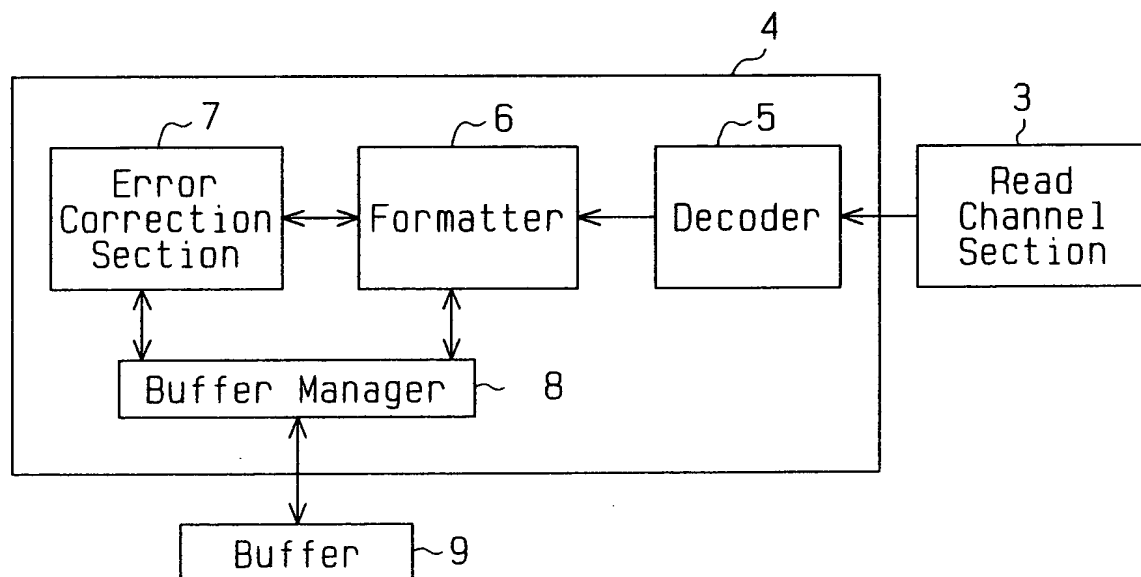


Fig.3

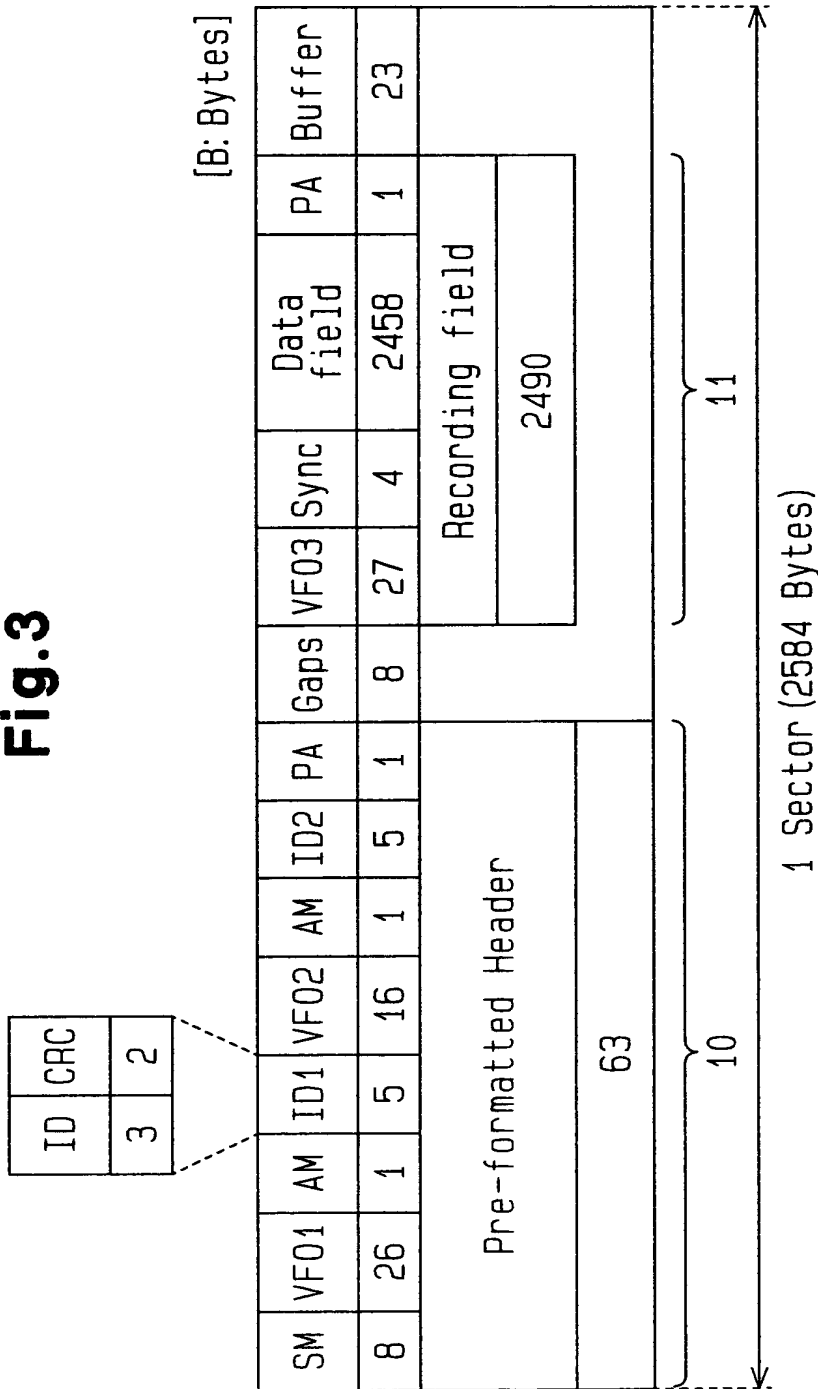


Fig.4 (Prior Art)

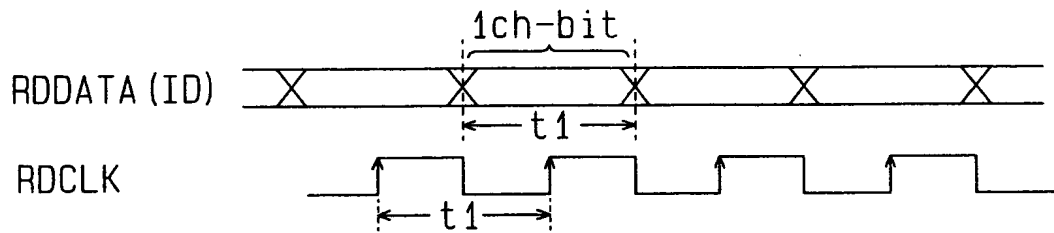


Fig.5 (Prior Art)

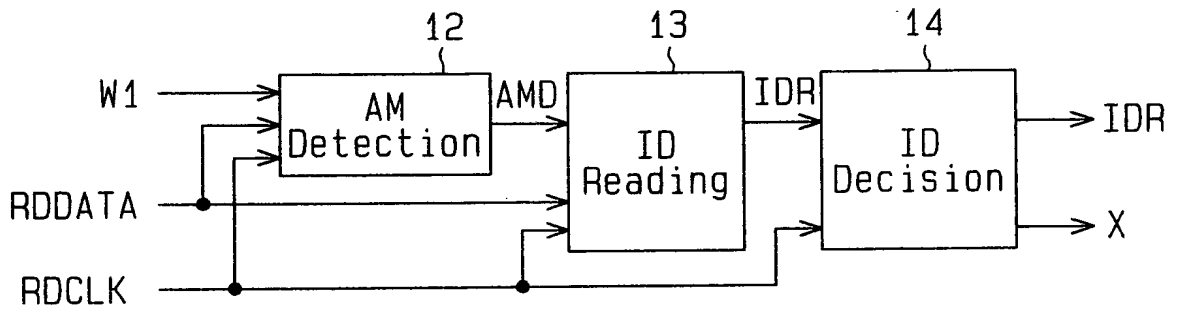


Fig.6

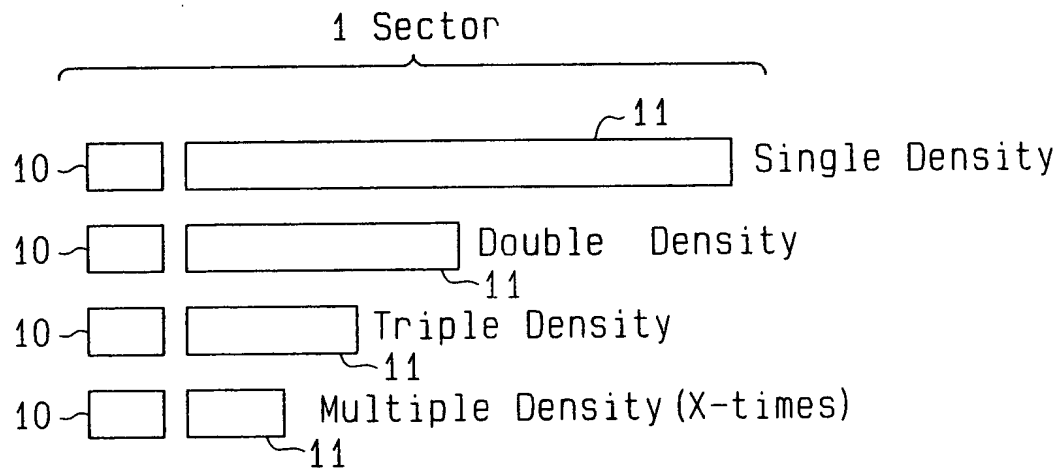


Fig.7

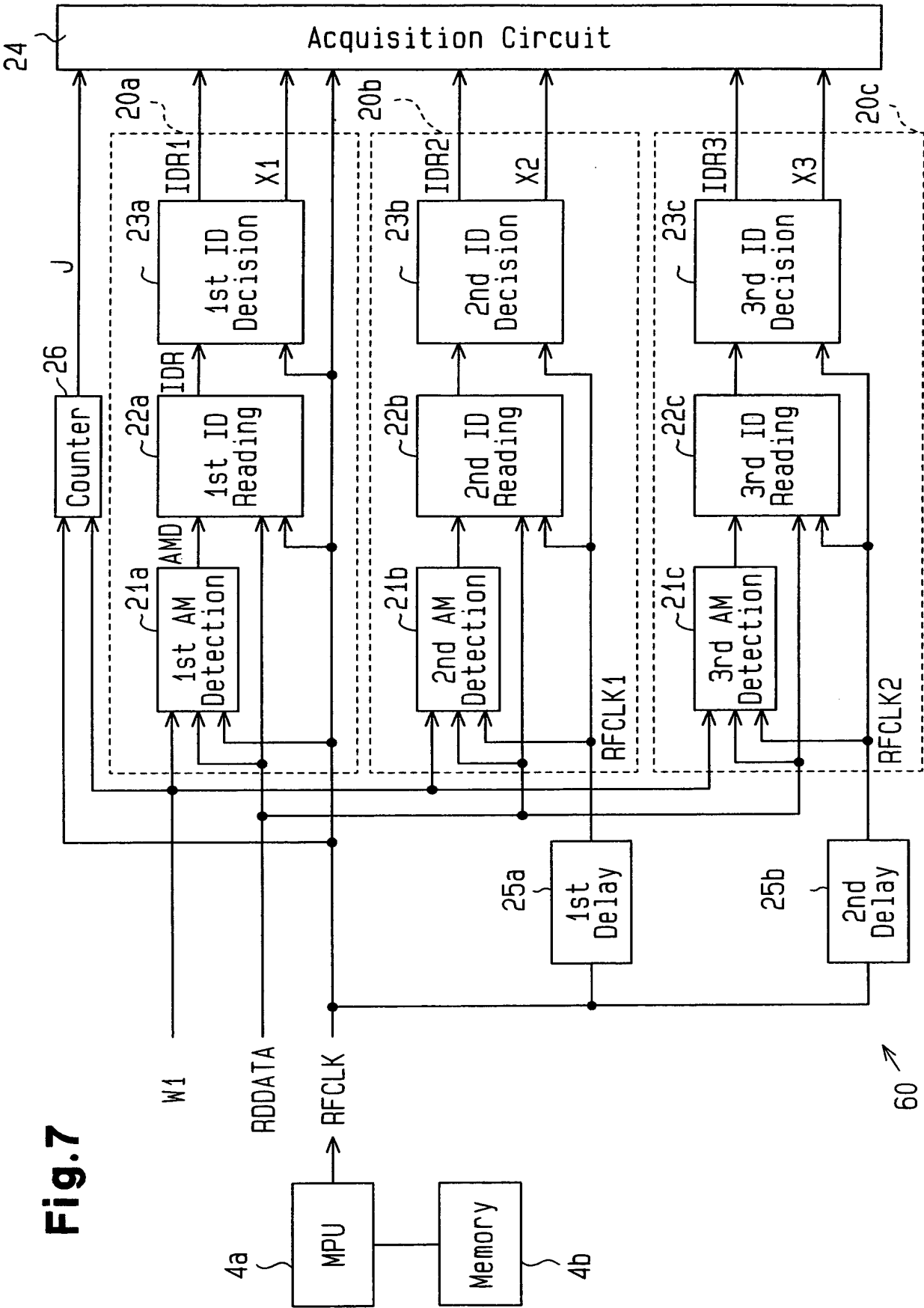


Fig.8A

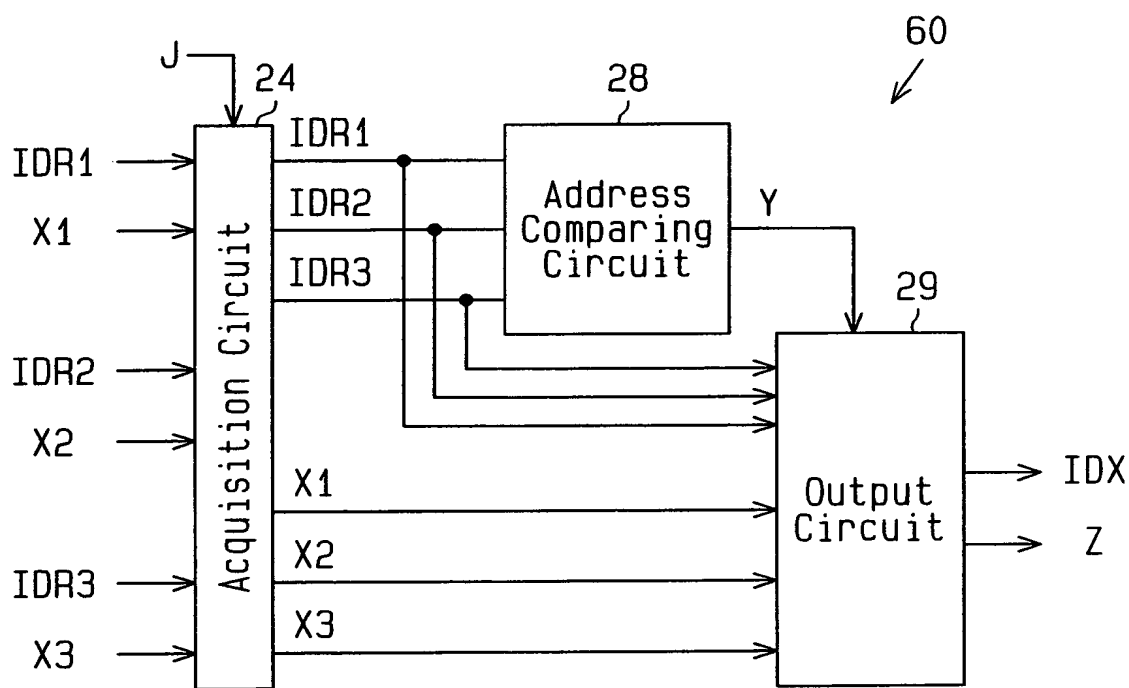


Fig.8B

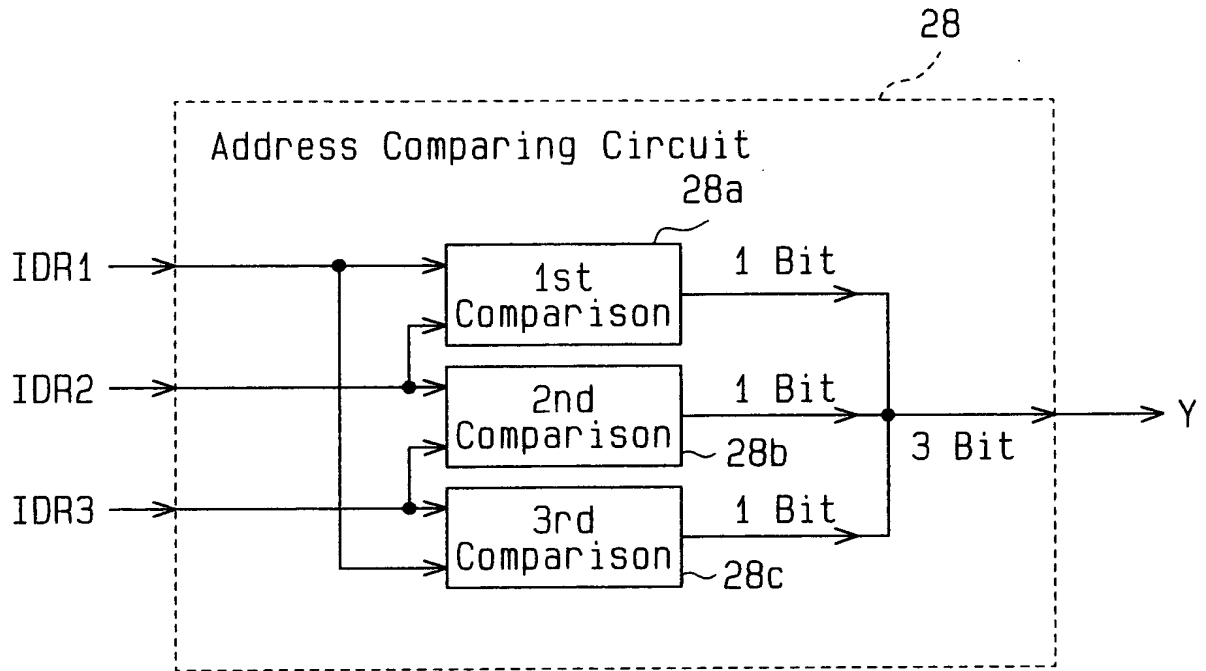


Fig.8C

Y[3 Bit]			IDR1	IDR2	IDR3
IDR1, 2 Matched?	IDR3, 1 Matched?	IDR2, 3 Matched?			
0	0	0	IDA	IDB	IDC
0	0	1	IDA	IDB	IDB
0	1	0	IDA	IDB	IDA
0	1	1	--	--	--
1	0	0	IDA	IDA	IDC
1	0	1	--	--	--
1	1	0	--	--	--
1	1	1	IDA	IDA	IDA

Fig.9

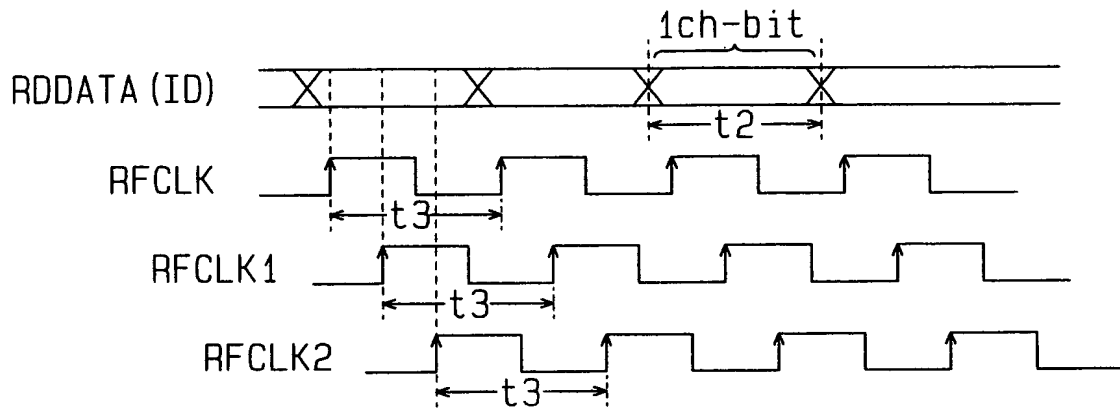


Fig.10

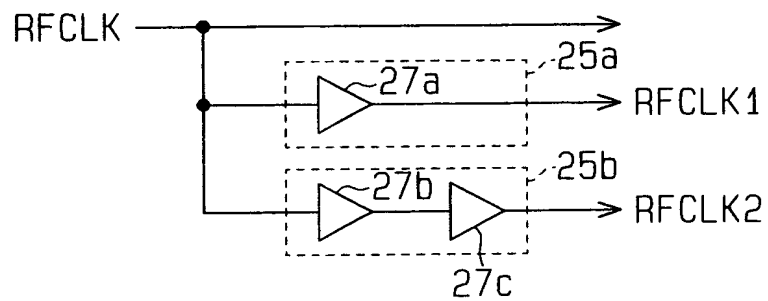


Fig.11

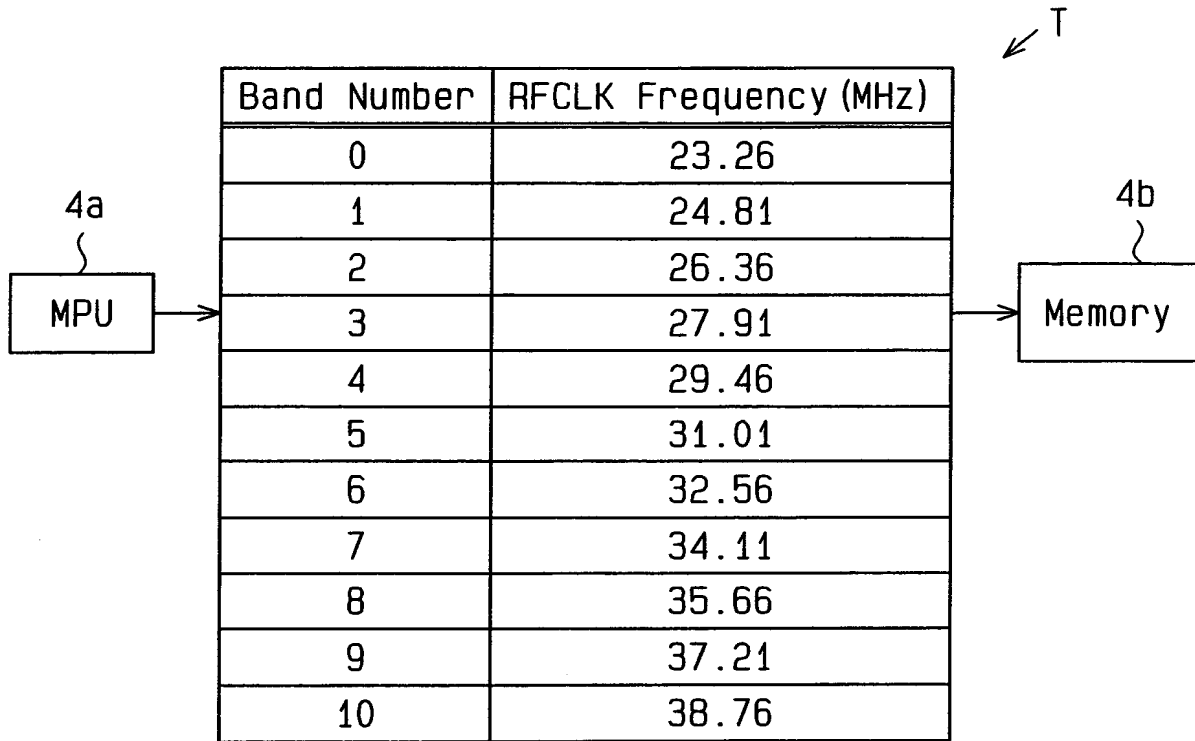


Fig.12

No .	①	②	③	④	⑤	⑥	⑦
IDR1	IDA	IDA	IDA	xxx	IDA	IDA	IDA
X1	○	○	○	E	○	○	○
IDR2	IDA	IDA	xxx	xxx	IDA	IDB	IDB
X2	○	○	E	E	○	○	○
IDR3	IDA	xxx	xxx	xxx	IDB	xxx	IDC
X3	○	E	E	E	○	E	○
IDX	IDA	IDA	IDA	xxx	IDA	xxx	xxx
Z	○	○	○	E	○	E	E

Fig.13

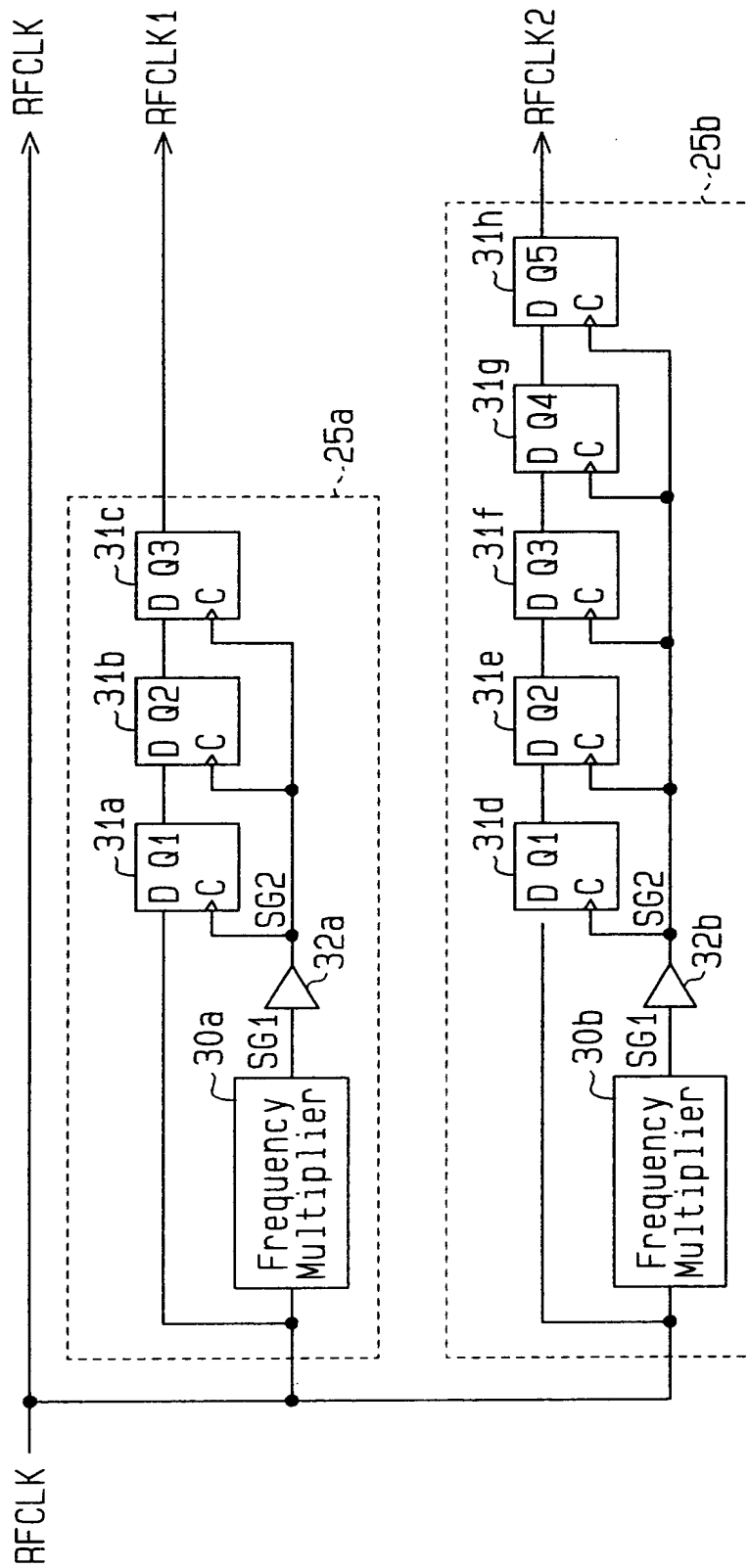


Fig.14

